

FIG. 1

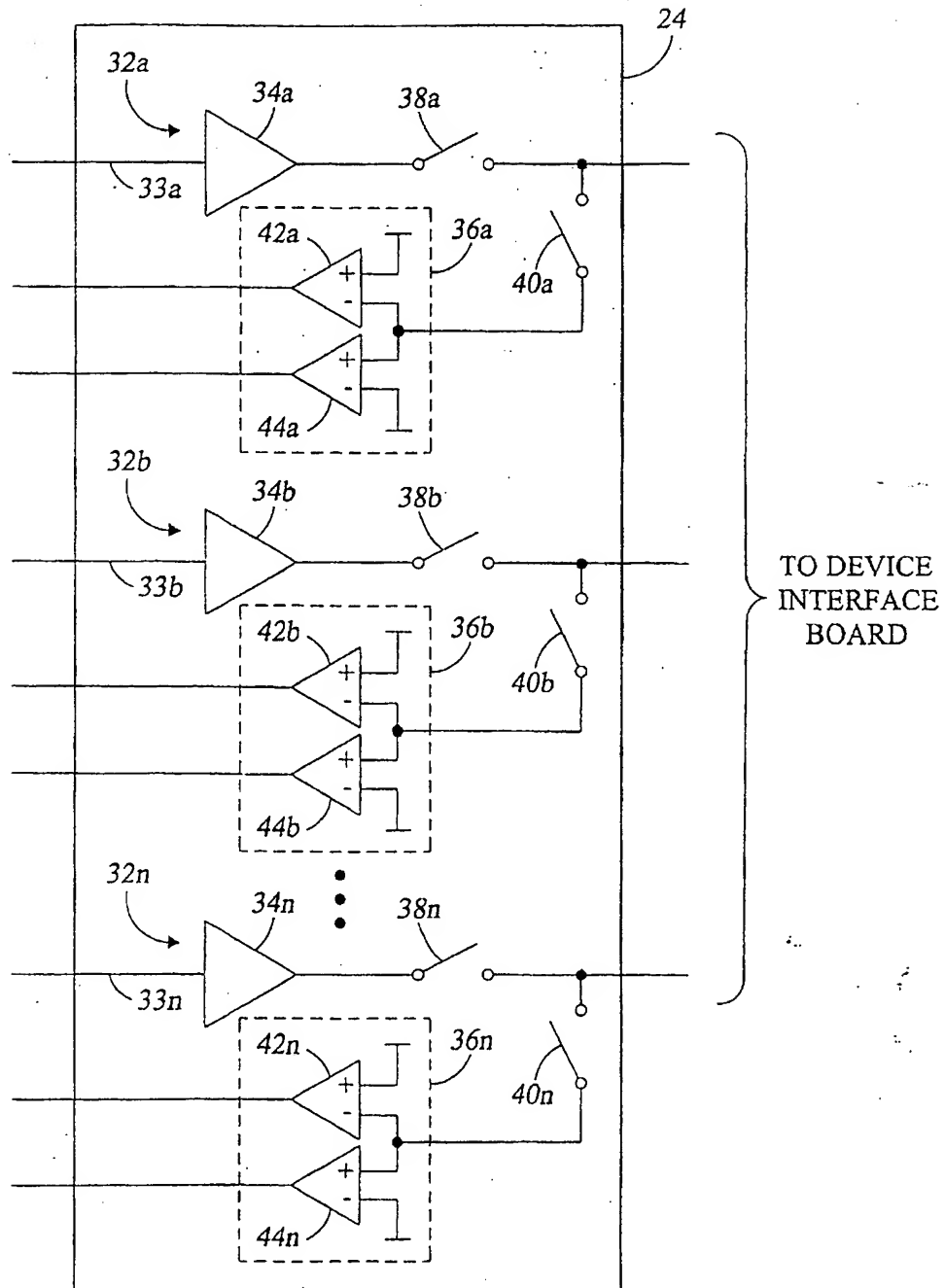


FIG. 2

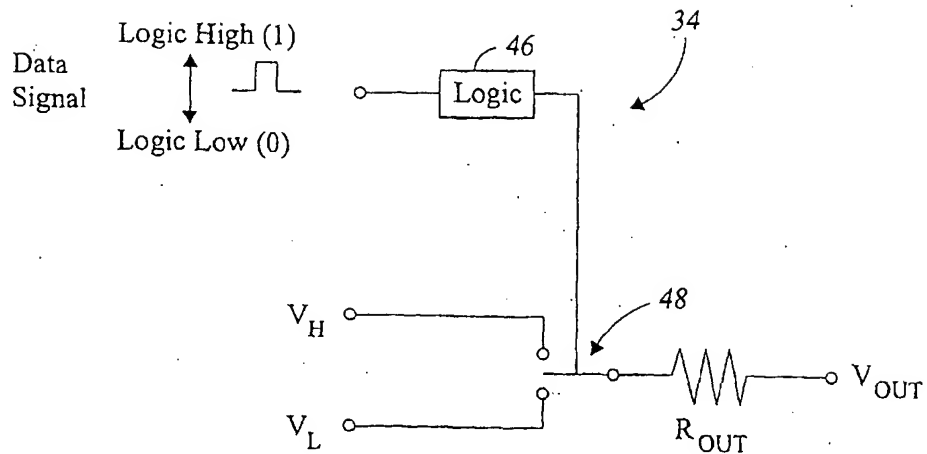


FIG. 3

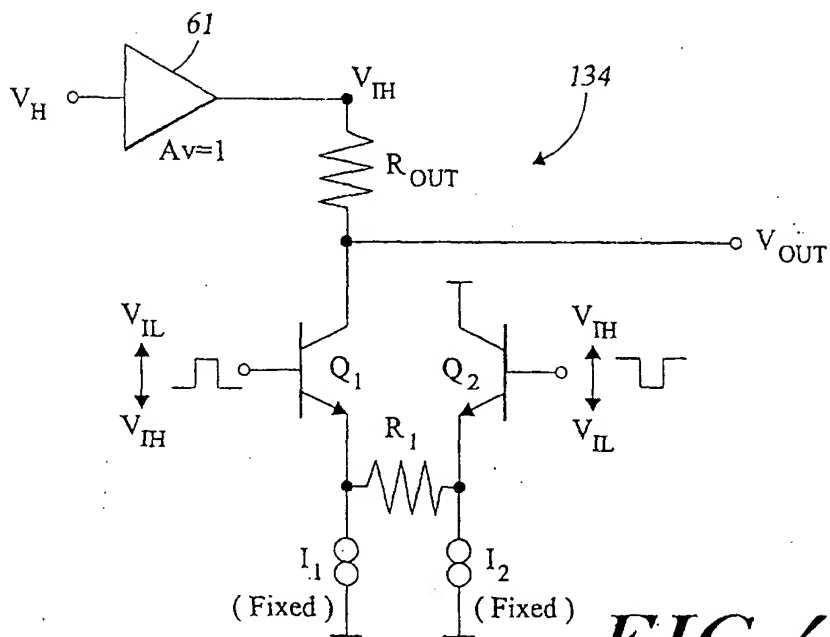


FIG. 4

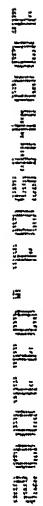
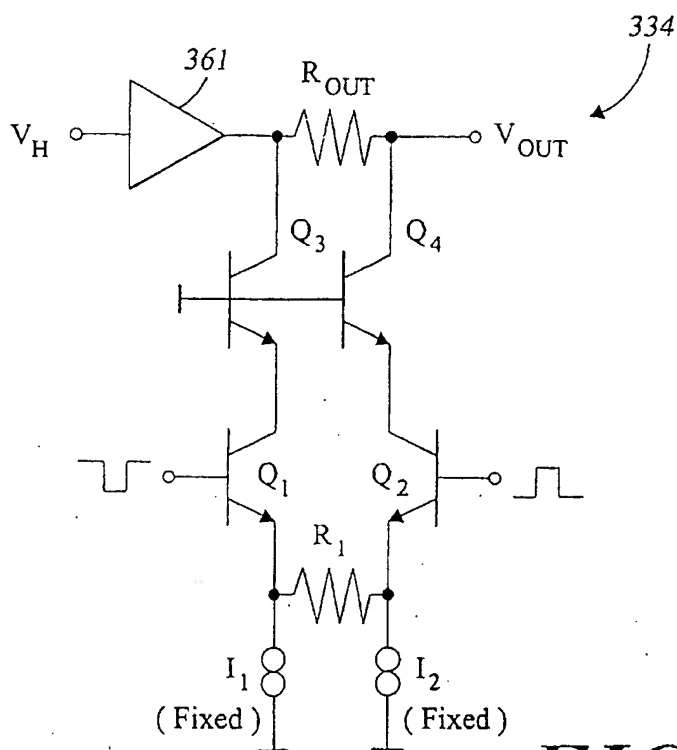


FIG. 6



20070 TESTH00T

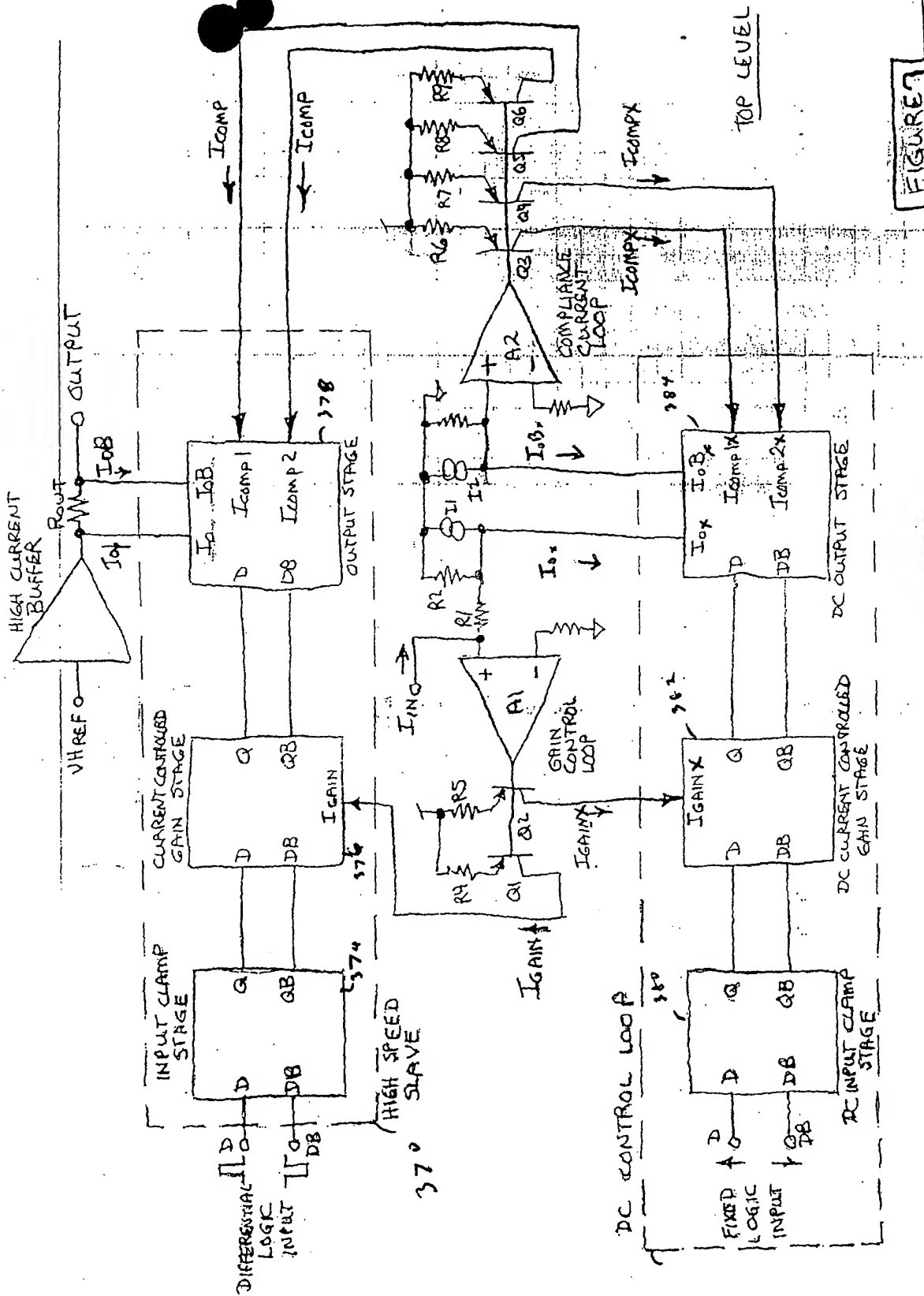
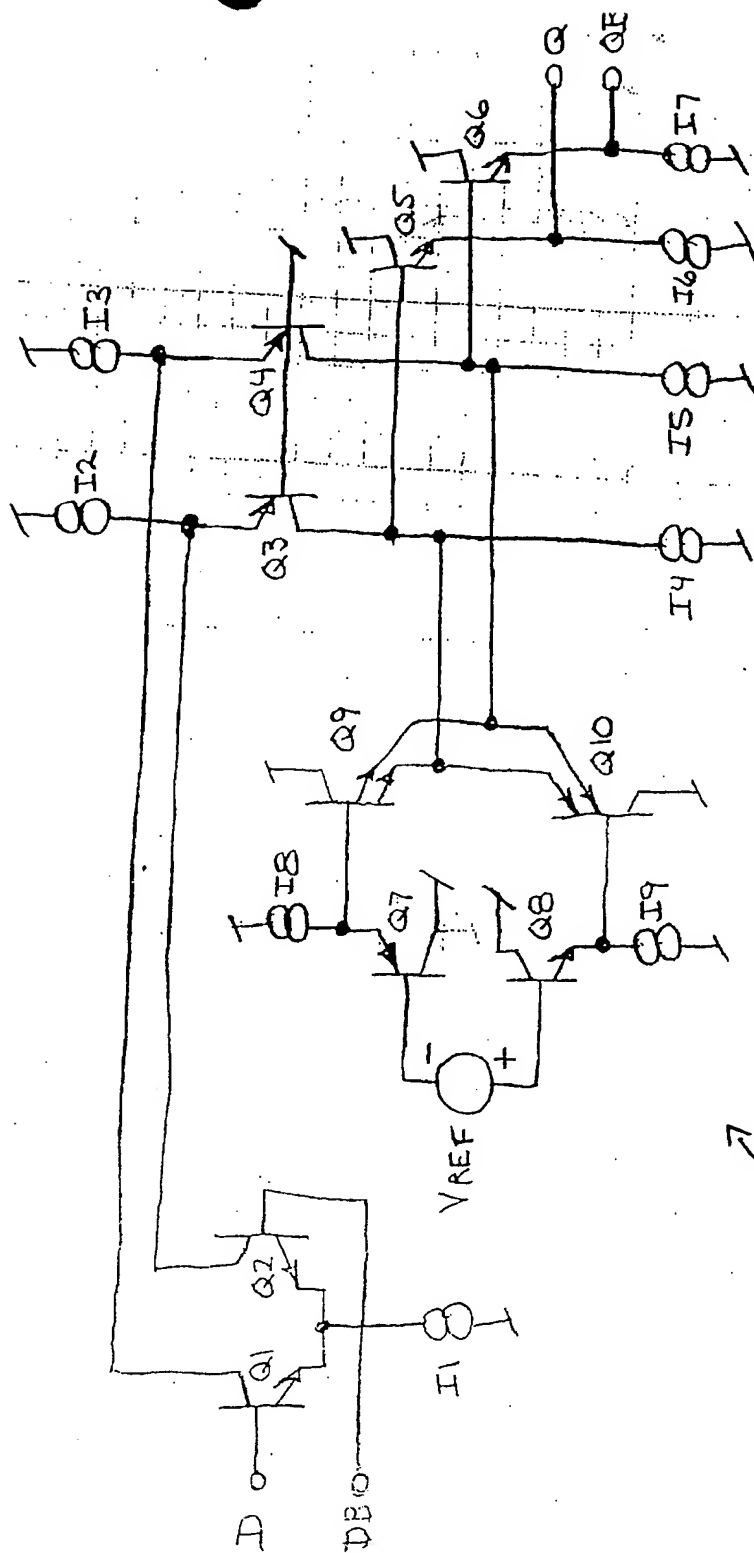
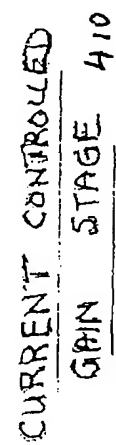


FIGURE 7

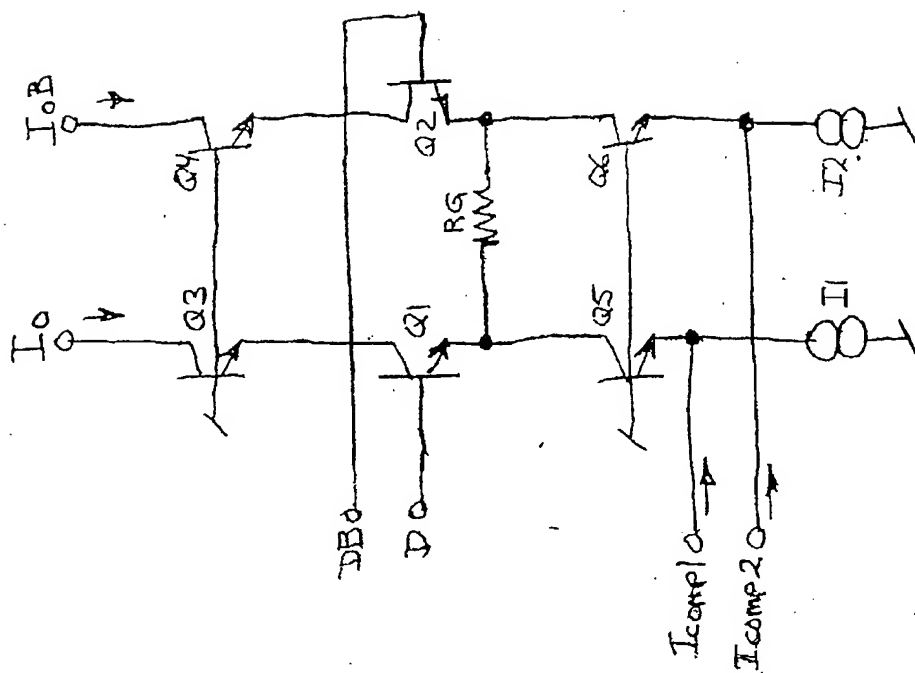


INPUT CLAMP STAGE

CURRENT CONTROLLED
GAIN STAGE 410



CURRENT CONTROLLED
GAIN STAGE 410



2

OUTPUT STAGE 420

FIGURE 10

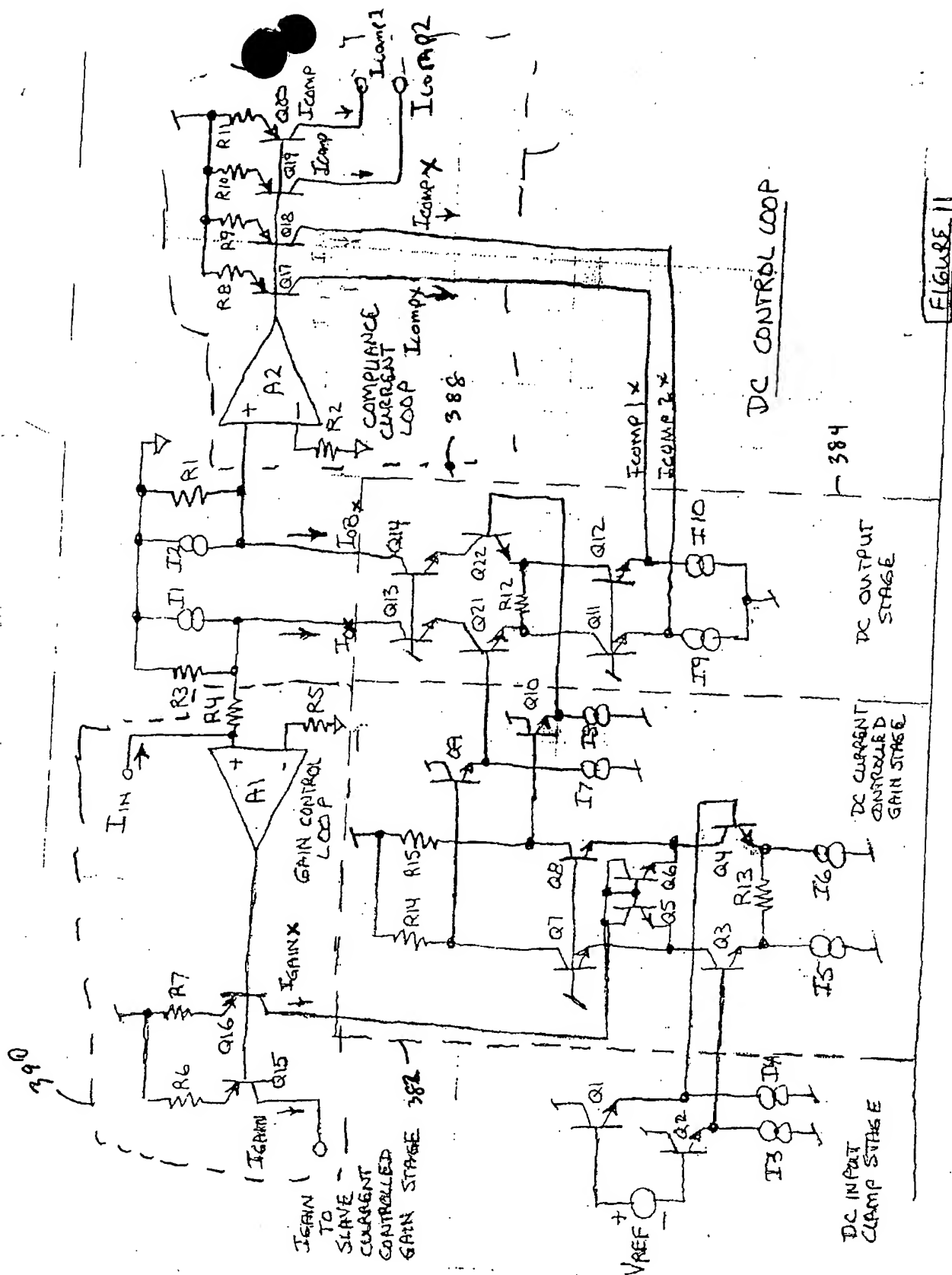


FIGURE 11